|  |  |
| --- | --- |
|  | **DEPARTMENT OF COMPUTER ENGINEERING** |

**Experiment No. 04**

|  |  |
| --- | --- |
| Semester | S.E-Semester III – Computer Engineering |
| Subject | Digital Logic and Computer Architecture |
| Subject Professor In-charge | Prof. Avinash Shrivas |
| Assisting Teachers | Prof. Avinash Shrivas |

|  |
| --- |
| Student Name – Deep Salunkhe |
| Roll Number – 21102A0014 |
| Division and Batch – Division A, Batch 1 |
| Date of Implementation – 17/07/2022 |
| Experiment Title: To implement XOR gate. |
| **Theory:**  NAND and NOR gates are also known as universal gates as all the gates can be recreated using these gates Here, we have created XOR Gates |
|  |

|  |
| --- |
| **Implementation** |
|  |